

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re National Stage Application of PCT/JP01/05578

MAEKAWA et al.

Appln. No.: 10/069,274

Filed: February 25, 2002



Group Art Unit: 2812

Examiner: Not Yet Assigned

FOR: FABRICATION PROCESS OF A SEMICONDUCTOR DEVICE

\* \* \* \* \*

September 3, 2002

**INFORMATION DISCLOSURE STATEMENT**

Hon. Commissioner of Patents  
Washington, DC 20231

Sir:

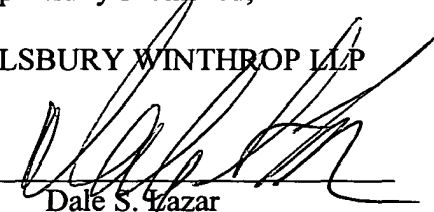
Enclosed herewith are a PTO-1449 and a copy of a U.S. patent that was cited in an Office Action, dated June 30, 2002, in a corresponding Taiwanese application, for the Examiner's consideration.

Consideration of the foregoing and the return of a copy of the enclosed PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited.

Respectfully submitted,

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**INFORMATION DISCLOSURE STATEMENT  
BY APPLICANT**

Applicant: MAEKAWA et al.

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Page 1 of 1

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**U.S. PATENT DOCUMENTS**

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
	AR 5,972,803	10/1999	SHU et al.			
	BR					
	CR					
	DR					
	ER					
	FR					
	GR					
	HR					
	IR					
	JR					
	KR					
	LR					
	MR					
	NR					

**FOREIGN PATENT DOCUMENTS**

	Document Number	Date MM/YYYY	Country	Inventor Name	English Abstract		Translation Readily Available	
					Enclosed	No	Enclose	No
	OR							
	PR							
	QR							
	RR							
	SR							
	TR							
	UR							
	VR							
	WR							
	XR							

**OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)**

	YR			
	ZR			
	AAR			
	BBR			
	CCR			
	DDR			

Examiner

Date Considered:

\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.